

**PATENT NUMBER**

# U.S. UTILITY PATENT APPLICATION

I.P.E. PATENT DATE  
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APPLICATION NO. 09/800627	CONT/PRIOR F	CLASS 257	SUBCLASS S7	ART UNIT <del>2811</del> 2850	EXAMINER Thomas
<p>APPLICANTS Koichiro Tanaka Hideto Ohnuma Ching Kokubo</p> <p>TITLE Semiconductor device and manufacturing method thereof</p>					

PTO-2040  
12/99

**PREPARED AND APPROVED FOR ISSUE**

## ISSUING CLASSIFICATION

ISSUING CLASSIFICATION												
ORIGINAL					CROSS REFERENCE(S)							
CLASS		SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> a) The term of this patent subsequent to _____ (date) has been disclaimed.	<div>_____ (Assistant Examiner) (Date)</div> <div>_____ (Primary Examiner) (Date)</div>			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____				<div><b>ISSUE FEE</b></div> <div>Amount Due      Date Paid</div>	
<input type="checkbox"/> c) The terminal _____ months of this patent have been disclaimed.	<div>_____ (Legal Instruments Examiner) (Date)</div>			<b>ISSUE BATCH NUMBER</b>	
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